

FIG. 1

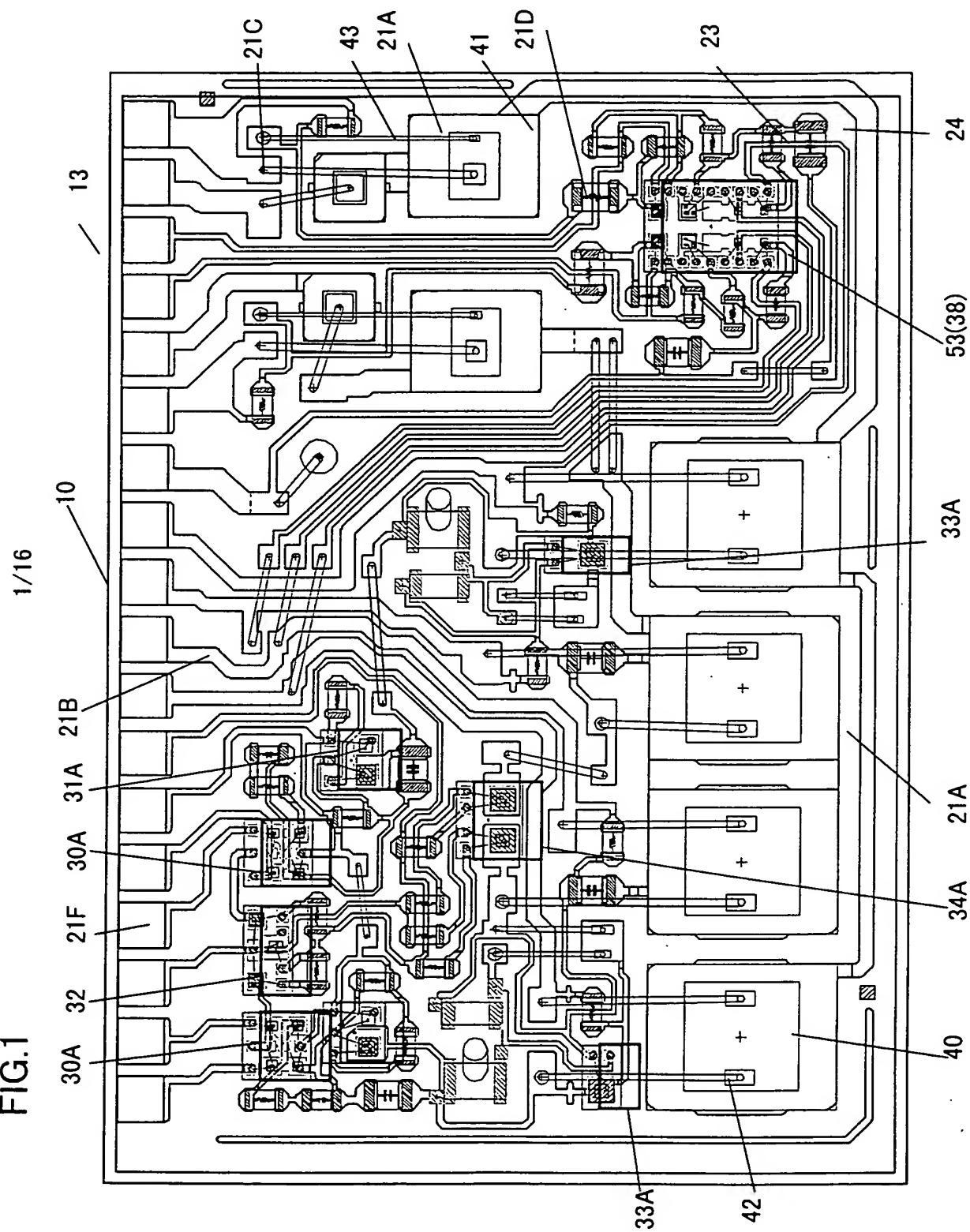


FIG.2

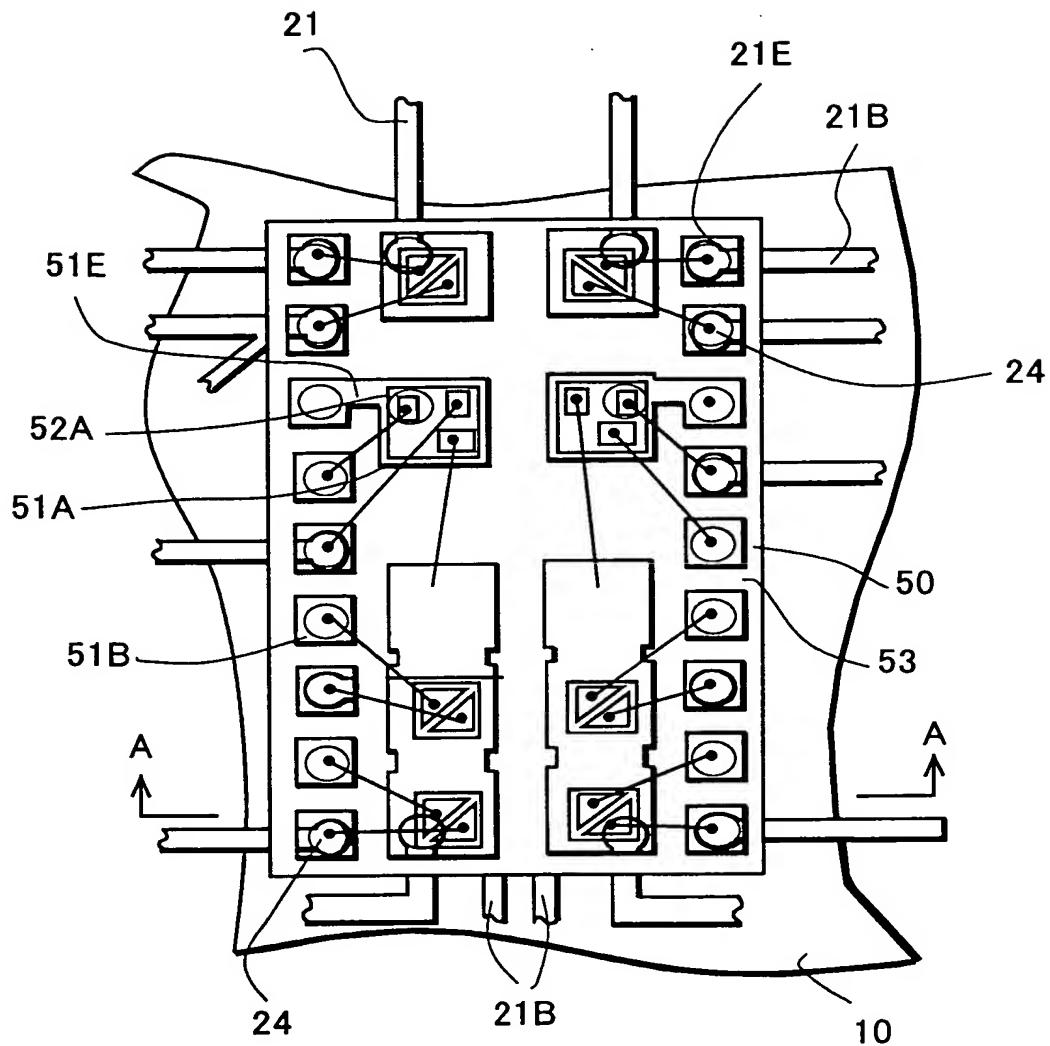


FIG.3A

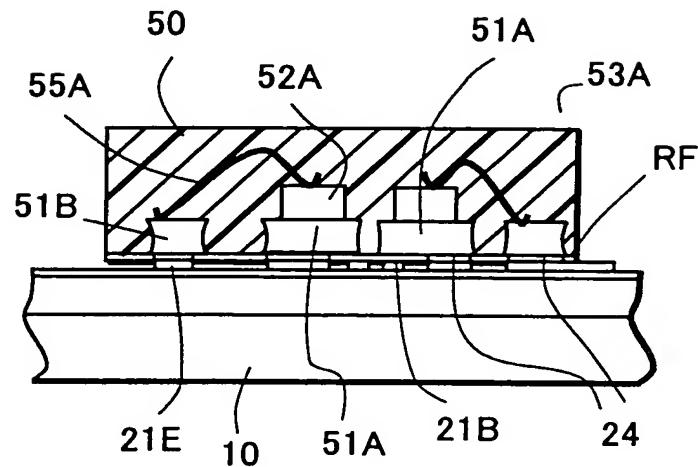


FIG.3B

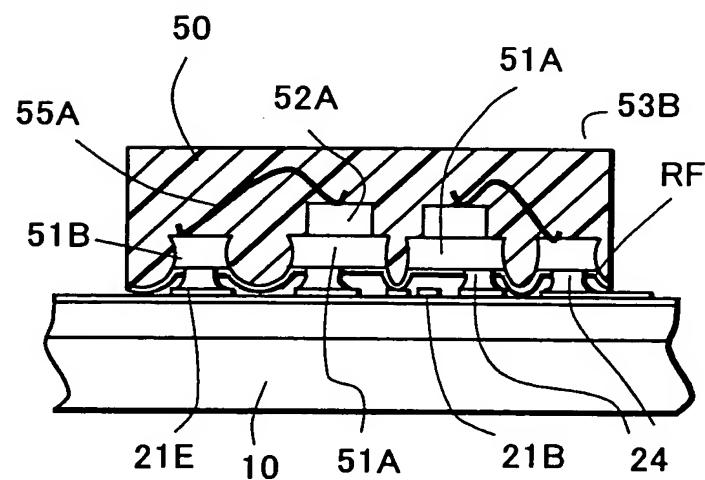


FIG.3C

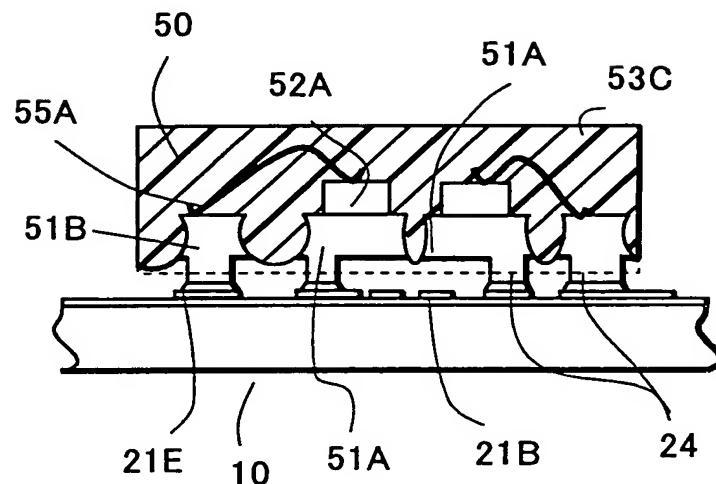


FIG.4

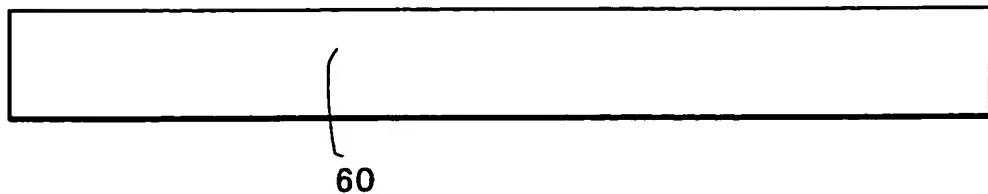


FIG.5

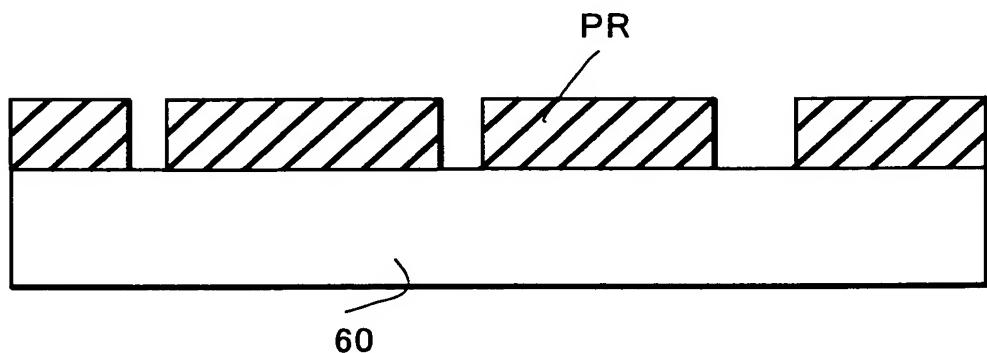


FIG.6A

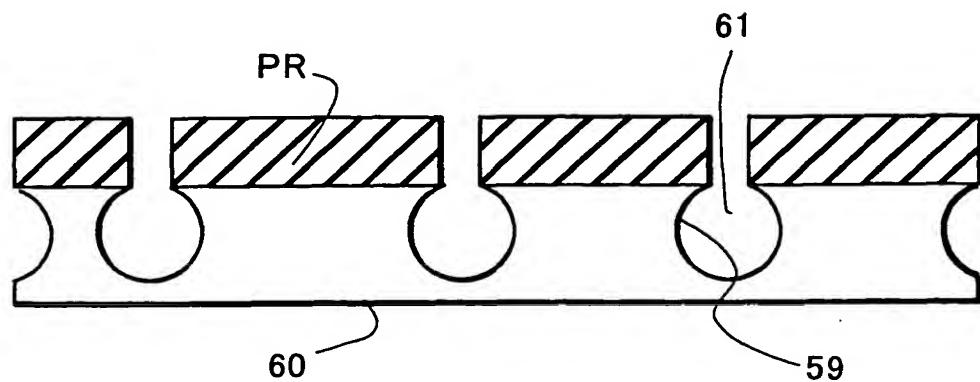


FIG.6B

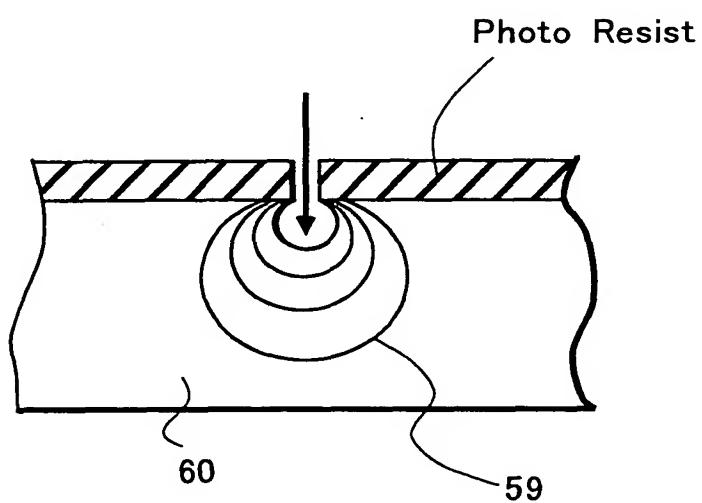


FIG.7

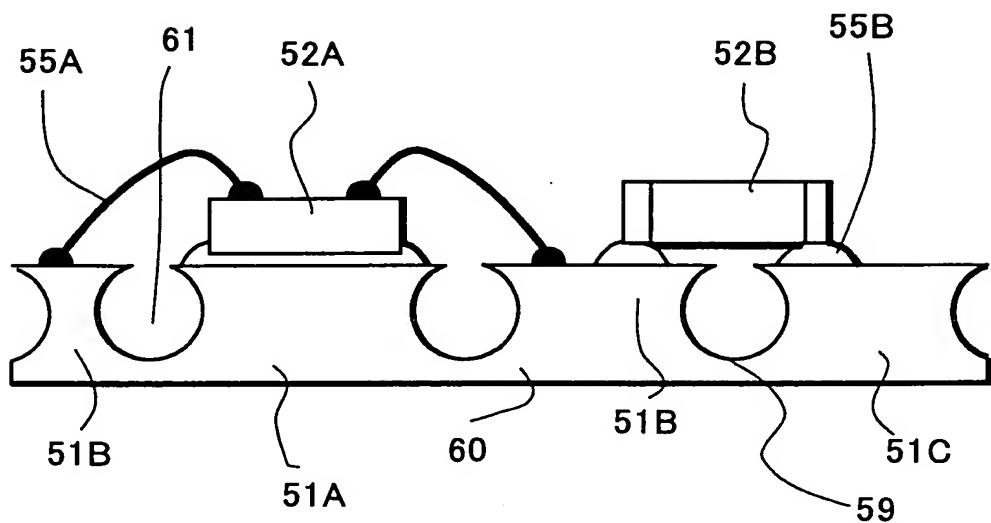


FIG.8

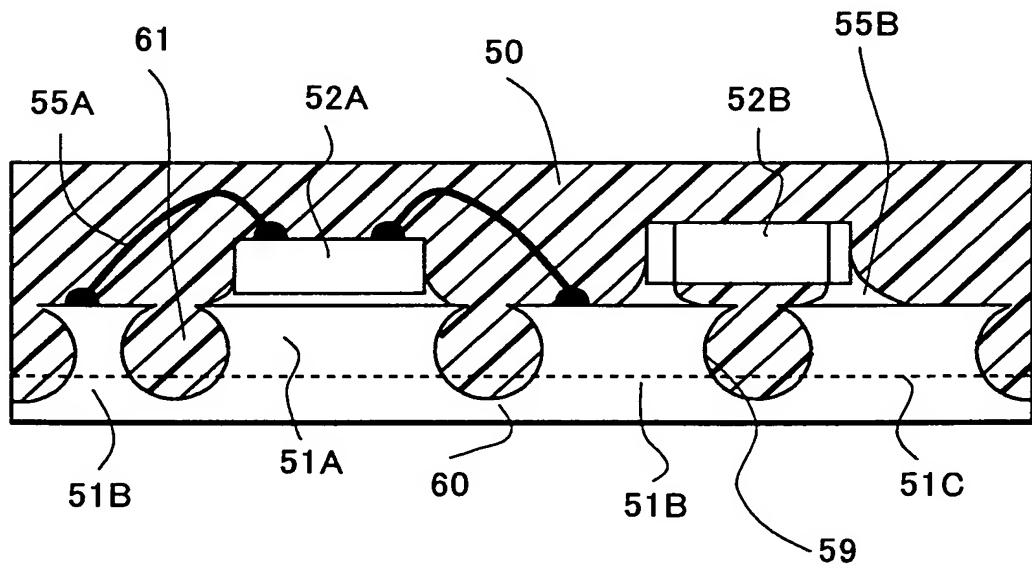


FIG.9A

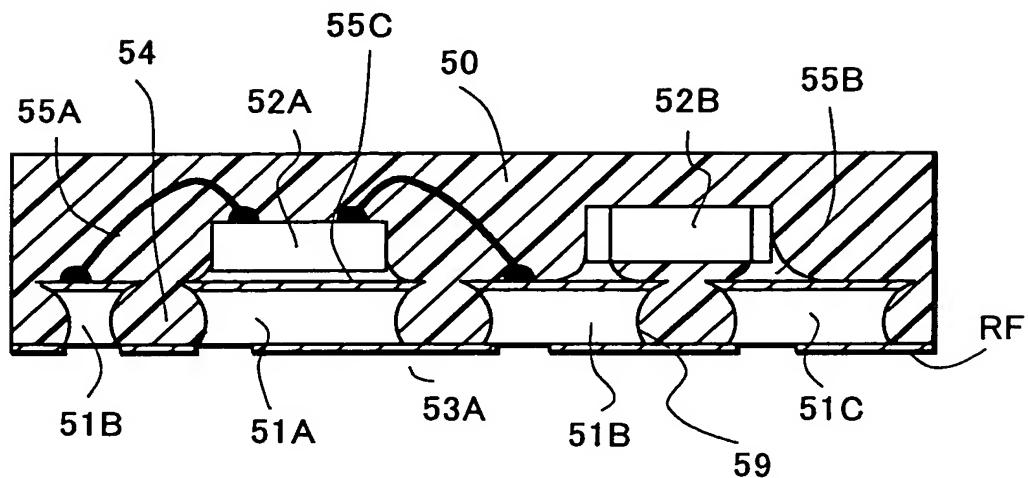


FIG.9B

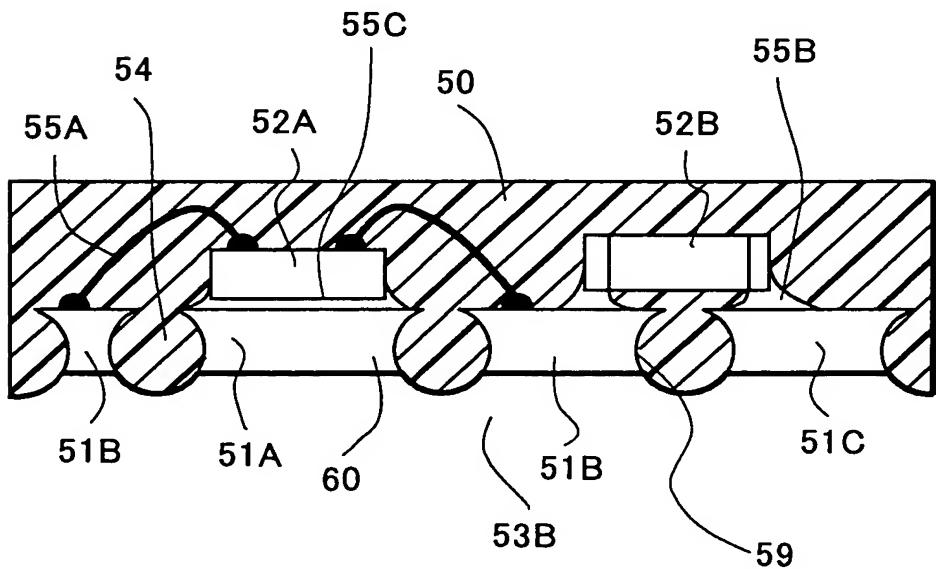


FIG.9C

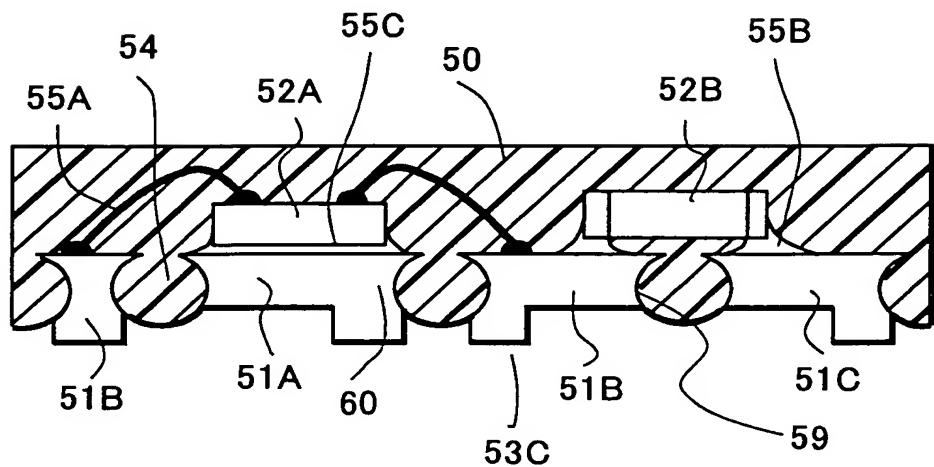


FIG.10A

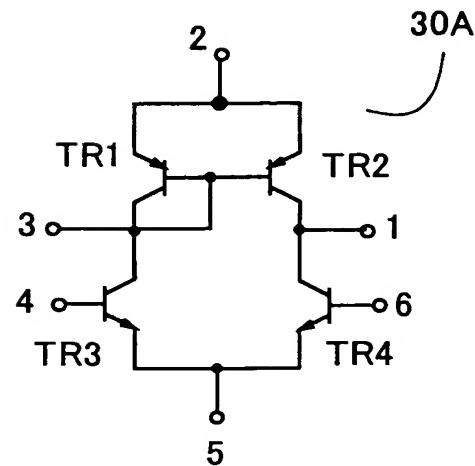


FIG.10B

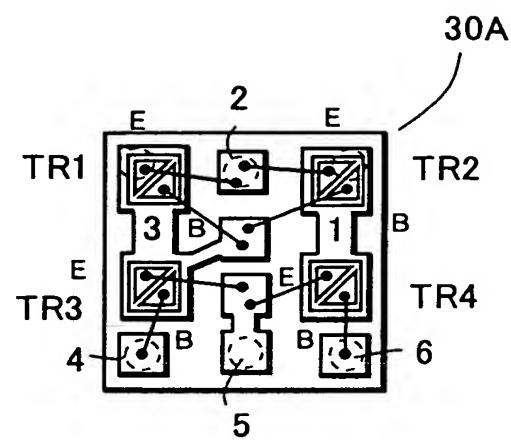


FIG.11A

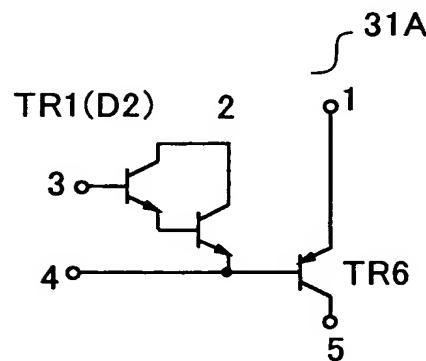


FIG.11B

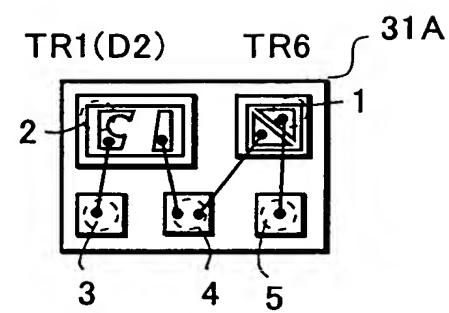


FIG.12A

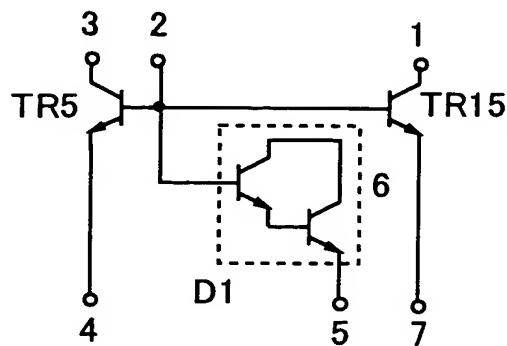


FIG.12B

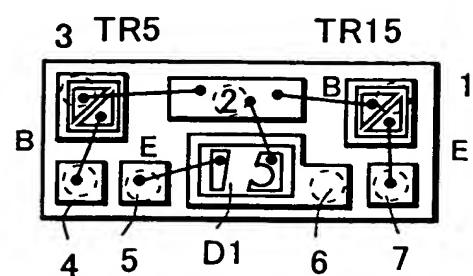


FIG.13A

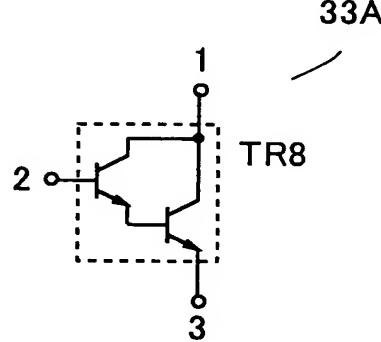


FIG.13B

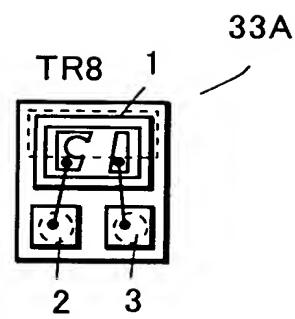


FIG.14A

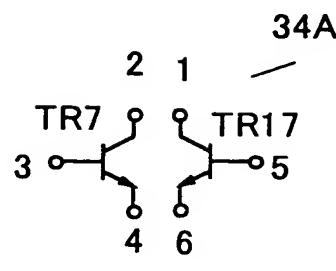


FIG.14B

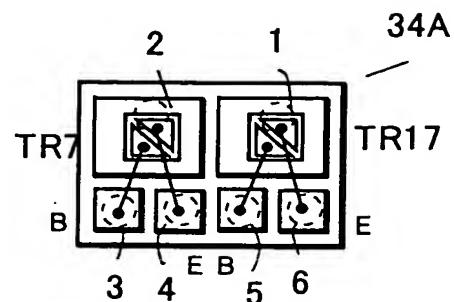


FIG.15A

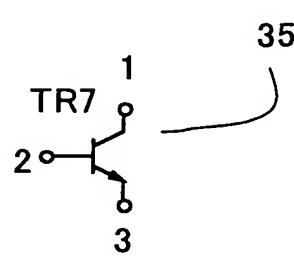


FIG.15B

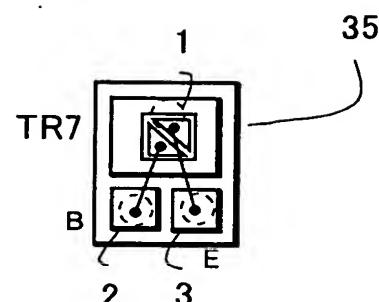


FIG.16A

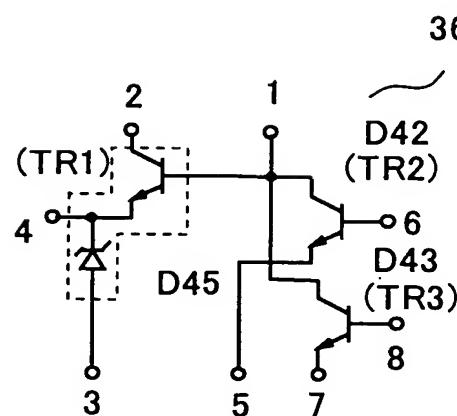


FIG. 16B

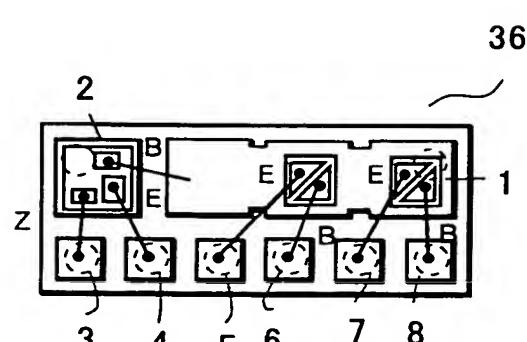


FIG.17A

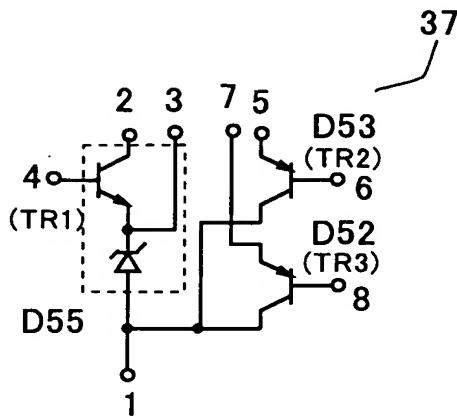


FIG.17B

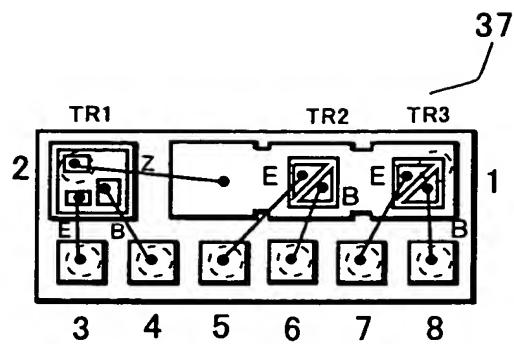


FIG.18A

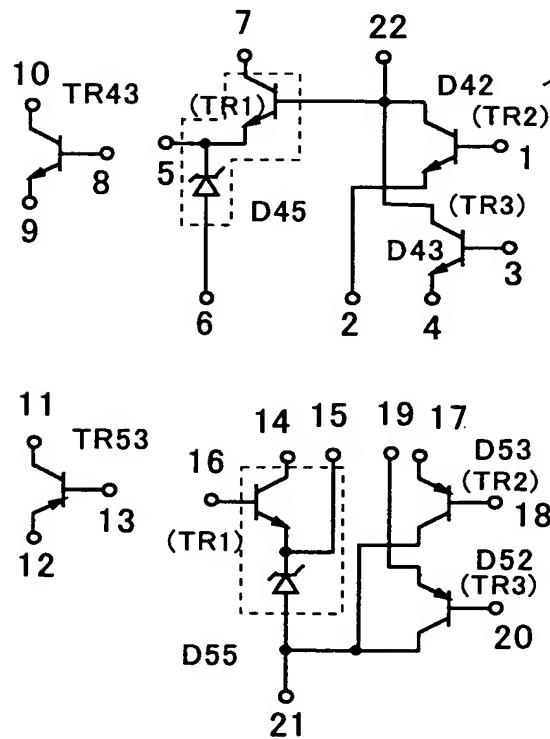


FIG.18B

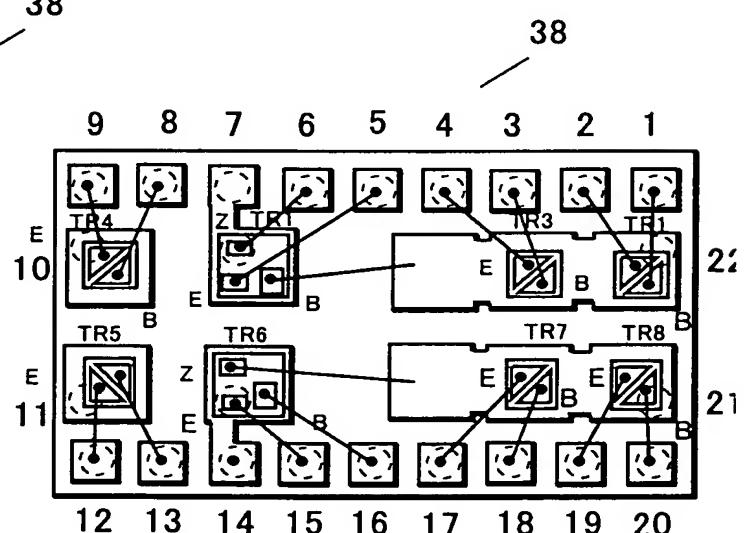


FIG.19

14/16

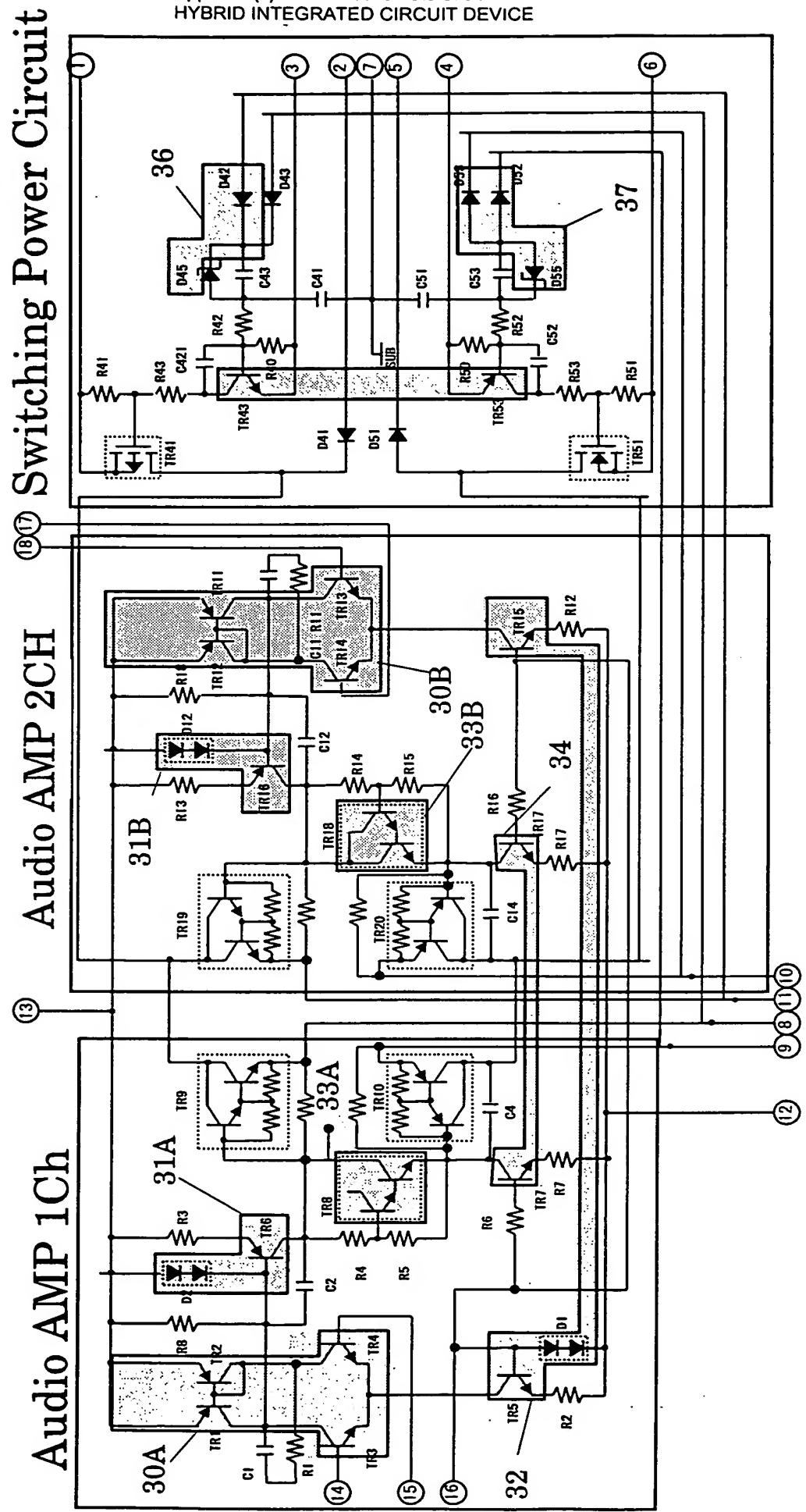


FIG.20
PRIOR ART

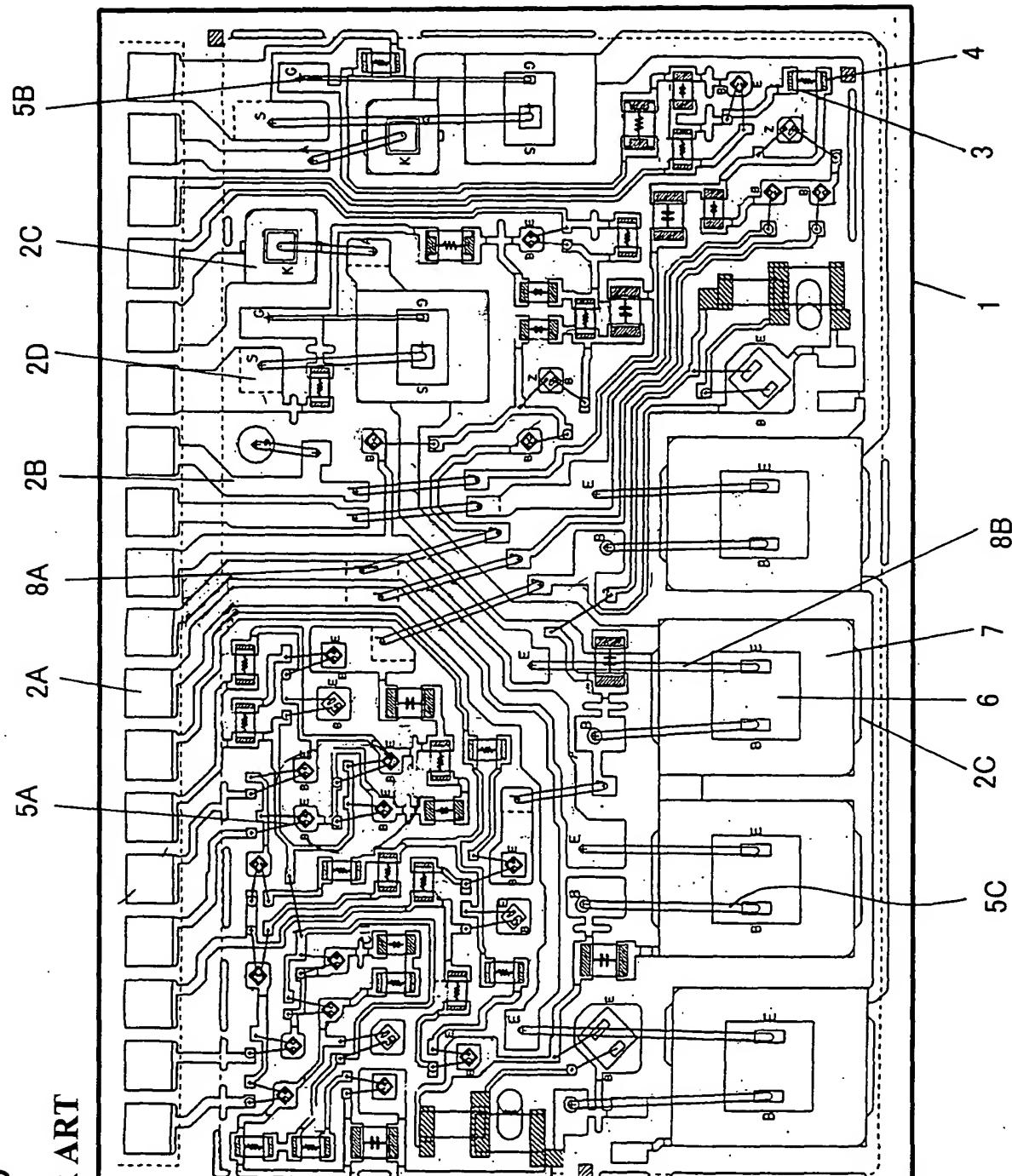
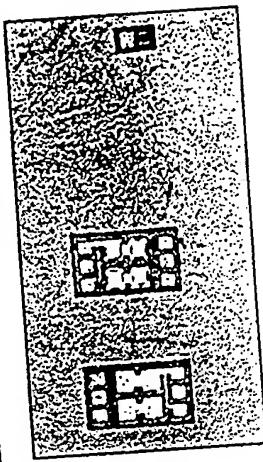


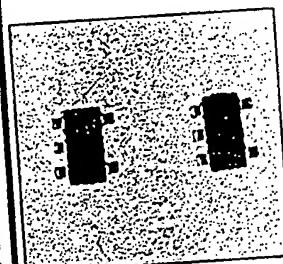
FIG.21

Example:Differential Current Mirror Circuit

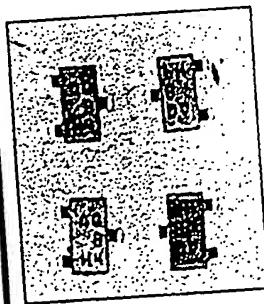
Semiconductor Device of
this Invention



Complexed
SMD



Single SMD



Equivalent Circuit

